

DOCKET NO.: MSFT-1086

PATENT

#119  
P/B  
with  
Revocation  
L. Chase  
3/24

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Matthew Papakipos et al

Serial No.: 08/845,526

Group Art Unit: 2671

Filing Date: 4/25/97

Examiner: M. PADMANABHAN

For: A METHOD AND SYSTEM FOR EFFICIENTLY DRAWING NURBS  
SURFACES FOR 3D GRAPHICS

BOARD OF PATENT APPEALS  
AND INTERFERENCES

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Commissioner for Patents and Trademarks  
Washington, DC 20231

POWER OF ATTORNEY WITH REVOCATION

Revoking any and all powers of attorney heretofore given in the manner of the above-entitled application, the undersigned, assignee of the entire interest in the above-identified application, hereby appoints Steven J. Rocci, Registration No. 30,489, of the firm Woodcock Washburn LLP, One Liberty Place - 46<sup>th</sup> Floor, Philadelphia, Pennsylvania, as attorney, with full power of substitution and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith.

BEST AVAILABLE COPY

DOCKET NO.: MSFT-1006

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PATENT

In addition, the assignee also appoints the following attorneys listed below of MICROSOFT CORPORATION, One Microsoft Way, Redmond, Washington 98052 with full power of substitution and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent and to transact all business in the Patent and Trademark Office connected therewith.

Daniel D. Crouse

Registration No. 32,022

Send all future correspondence and address all telephone calls to:

Steven J. Rocci  
WOODCOCK WASHBURN LLP  
One Liberty Place - 46th Floor  
Philadelphia, PA 19103  
Telephone: (215)568-3100

**STATEMENT UNDER 37 C.F.R. 3.73(b)**

**MICROSOFT CORPORATION**, a corporation of the State of Washington,

states that it is the assignee of the entire right, title, and interest in the patent application/patent identified above by virtue of:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above.

☐ 1. The assignment was recorded in the Patent and Trademark Office at Reel \_\_\_\_\_ Frame \_\_\_\_\_.

☐ 2. The assignment has not yet been recorded. A copy of the assignment is attached.

OR

B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

DOCKET NO.: MSFT-1086

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PATENT

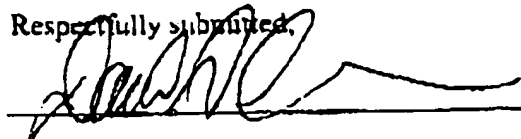
From the inventors to: Silicon Graphics, Inc. This assignment has not been recorded. A copy thereof is attached.

From: Silicon Graphics, Incorporated To: Microsoft Corporation, dated September 28, 2001. This assignment has not yet been recorded. A copy thereof is attached.

Copies of assignments or other documents in the chain of title are attached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignees identified above. The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee, **MICROSOFT CORPORATION**.

Respectfully submitted,



Date: 3-7-03

Name: DANIEL D. CROUSE  
Title: Assistant Secretary

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UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
ASSISTANT SECRETARY AND COMMISSIONER  
OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231

DECEMBER 20, 2002

PTAS

WOODCOCK WASHBURN LLP  
STEVEN J. ROCCI  
ONE LIBERTY PLACE, 46TH FLOOR  
PHILADELPHIA, PENNSYLVANIA 19103-7301

**\*700021680A\***

~~\*700021680A\*~~

UNITED STATES PATENT AND TRADEMARK OFFICE  
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 12/19/2002

REEL/FRAME: 013307/0049  
NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:  
PAPAKIPOS, MATTHEW N.

DOC DATE: 04/23/1997

ASSIGNOR:  
GOSSETT, CARROLL PHILIP

DOC DATE: 04/21/1997

ASSIGNOR:  
PAPPAS, CHRISTIAN

DOC DATE: 04/21/1997

ASSIGNOR:  
MORETON, HENRY P

DOC DATE: 04/22/1997

ASSIGNOR:  
WILLIAMSON, ROBERT J.

DOC DATE: 04/21/1997

ASSIGNEE:  
SILICON GRAPHICS, INC.  
2011 NORTH SHORELINE BOULEVARD  
MOUNTAIN VIEW, CALIFORNIA 94043

013307/0049 PAGE 2

SERIAL NUMBER: 08845526  
PATENT NUMBER:

FILING DATE: 04/25/1997  
ISSUE DATE:

ALLYSON PURNELL, EXAMINER  
ASSIGNMENT DIVISION  
OFFICE OF PUBLIC RECORDS

19/2002  
00021680  
PATENTS UNIT

To The Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Matthew N. Papakipos  
First Assignor  
April 23, 1997  
Date of Execution

Carroll Philip Gossen  
Second Assignor  
April 21, 1997  
Date of Execution

Christian Pappas  
Third Assignor  
April 21, 1997  
Date of Execution

Henry P. Morison  
Fourth Assignor  
April 22, 1997  
Date of Execution

Robert J. Williamson  
Fifth Assignor  
April 21, 1997  
Date of Execution

Sixth Assignor  
Date of Execution

2. Name and address of receiving party(ies):

Name: Silicon Graphics, Inc

Street Address: 2011 North Shoreline Boulevard

City: Mountain View

State: California ZIP 94043

Additional name(s) & address attached? ☐ YES ☒ NO

3. Nature of Conveyance (check only one):

☒ New Assignment  
☐ License Agreement  
☐ Change of Name

☐ Merger  
☐ Security Agreement  
☐ Other.

☐ Cross-reference of Assignment filed in United States  
Application No. \_\_\_\_\_, recorded on \_\_\_\_\_ at Reel  
No. \_\_\_\_\_ and Patent No. \_\_\_\_\_

Additional name(s) of conveying party(ies) attached? ☐ YES ☒ NO

4. Identification of Application number(s), patent number(s) OR date of execution of application to which assignment refers:

Application Nos.:  
08/845,526 filed April 25, 1997

Patent Nos.:

Date application was signed by the first named executing inventor

(Fill in this date only if assignment is filed together with a new application)

5. Name and address of party to whom correspondence concerning documents should be mailed:

Name: Steven J. Ricci  
WOODCOCK WASHBURN LLP  
One Liberty Place - 46th Floor  
Philadelphia, Pennsylvania 19103-7301  
215-568-3100

6. Total number of applications and patents involved: One

7. Total fee (37 CFR 3.41). \$40.00

☐ Enclosed  
☒ Authorized to be charged to Deposit Account Number 23-3050

8. Please charge any deficiency or credit any overpayment to Deposit Account Number 23-3050

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Lawler Oct 2002  
Name of Person Signing/Key No.

Signature

Date

Total number of pages including cover sheet, assignment document and additional pages attached thereto: 3

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents and Trademarks  
Box Assignments  
Washington, D.C. 20531

## Patent

Docket No.: SGI 15-4-453.00

## Assignment to Silicon Graphics, Inc.

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I  
Matthew N. Papakipos, Carroll Philip Gossert, Christian Pappas.  
Henry P. Moreton, Robert J. Williamson

do hereby sell, assign and transfer unto Silicon Graphics, Inc. (hereinafter called Silicon Graphics), a Delaware Corporation having its principal place of business at 211 North Shoreline Boulevard, Mountain View, California 94043-1989, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

A METHOD AND SYSTEM FOR EFFICIENTLY EVALUATING AND DRAWING NURBS SURFACES FOR 3D GRAPHICS

x filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or

Serial No.: \_\_\_\_\_ filed on \_\_\_\_\_ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore;

Further, we have agreed to assign to Silicon Graphics all inventions (except as otherwise limited by law) which relate to Silicon Graphics business and which were first conceived or actually reduced to practice during our employment by Silicon Graphics;

And for the above consideration, we agree promptly upon request of Silicon Graphics, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to Silicon Graphics, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me in lending such cooperation and assistance are paid by Silicon Graphics;

We further covenant with Silicon Graphics, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, we hereunto set our hands and seal.

In witness whereof, we hereunto set our hands and seal.

Inventor's Signature: [Signature] Date: 4/23/97

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventors Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventor's Signature: \_\_\_\_\_ Date: \_\_\_\_\_

Inventors Signature: \_\_\_\_\_ Date: \_\_\_\_\_

**Assignment to Silicon Graphics, Inc.**Patent  
Docket No.: SGI 15-4-453.00

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I  
Matthew N. Papakipos, Carroll Philip Gossett, Christian Pappas,  
Henry D. Moreton, Robert J. Williamson

do hereby sell, assign and transfer unto Silicon Graphics, Inc. (hereinafter called Silicon Graphics), a Delaware Corporation having its principal place of business at 2011 North Shoreline Boulevard, Mountain View, California 94043-1389, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

**A METHOD AND SYSTEM FOR EFFICIENTLY EVALUATING AND DRAWING NURBS SURFACES FOR 3D GRAPHICS**

X filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore; or

       Serial No.:                      filed on                                      and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefore;

Further, we have agreed to assign to Silicon Graphics all inventions (except as otherwise limited by law) which relate to Silicon Graphics business and which were first conceived or actually reduced to practice during our employment by Silicon Graphics;

And for the above consideration, we agree promptly upon request of Silicon Graphics, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to Silicon Graphics, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me in lending such cooperation and assistance are paid by Silicon Graphics;

We further covenant with Silicon Graphics, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, we hereunto set our hands and seal.

Inventor's Signature: _____	Date: _____
Inventor's Signature: <u>                                    </u> (CRG)	Date: <u>4/21/97</u>
Inventor's Signature: <u>Christian Pappas</u>	Date: <u>4/21/97</u>
Inventor's Signature: <u>[Signature]</u>	Date: <u>4/22/97</u>
Inventor's Signature: <u>Robert Williamson</u>	Date: <u>4/21/97</u>
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____
Inventor's Signature: _____	Date: _____



**EXHIBIT A****ASSIGNMENT**

WHEREAS, SILICON GRAPHICS, INC., a Delaware Corporation, (hereinafter referred to as "Assignor") owns all right, title and interest in and to the inventions, patents and patent applications (hereinafter referred to as the "Intellectual Property") identified in Schedule A attached hereto; and

WHEREAS, MICROSOFT CORPORATION, a Washington Corporation, (hereinafter referred to as "Assignee"), is desirous of acquiring the entire domestic and foreign right, title, and interest in and under the Intellectual Property.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor assigns and transfers to the Assignee and the Assignee's legal representatives, successors and assigns, pursuant to the terms of a concurrently executed Confidential Patent Assignment Agreement, its full and exclusive rights in and to the Intellectual Property in the U.S. and every foreign country and its entire right, title, and interest in and to the Intellectual Property and related applications (e.g., provisional applications, non-provisional applications, continuations, continuations-in-part, divisionals, reissues, reexaminations, National phase applications, including petty patent applications, and utility model applications) that may be filed in the United States and every foreign country on the Intellectual Property, and extensions or derivations thereof, both foreign and domestic, that may issue thereon, and we do hereby authorize and request the Commissioner of Patents to issue U.S. patents to the above-mentioned Assignee agreeably with the terms of this assignment document.

ASSIGNOR HEREBY AUTHORIZES the Assignee to insert in Schedule A to this assignment document the filing date and application number of any application if the date and number are unavailable at the time this document is executed.

UPON SAID CONSIDERATION, Assignor conveys to the Assignee the right to make application in its own behalf for protection of the Intellectual Property in the U.S. and countries foreign to the U.S. and to claim under the Patent Cooperation Treaty, the International Convention and/or other international arrangement for any such application the date of any earlier U.S. application (or any other application on the invention) to gain priority with respect to other applications.



## Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
Digital Media	US	5,506,624	Roaming Sample of Video Images	4/9/1996
	US	5,745,713	Movie-Based Facility for Launching Application Programs or Services	4/28/1998
	US	5,774,586	System and Method for Displaying Uniform Network Resource Locators Embedded in Time-Based Medium	6/30/1998
	US	5,987,509	System and Method for Displaying an Active URL During Playback of a Media File or Media Broadcast	11/16/1999
	US	5,808,882	Synchronized, Interactive Playback of Digital Movies Across a Network	9/14/1998
	US	5,943,347	Apparatus and Method for Error Concealment in an Audio Stream	8/24/1998
	US	6,005,650	High Performance Player for Distributed, Time-Based Media	12/21/1999
	US	6,147,895	System and Method for Combining Multiple Video Streams	11/14/2000
	US	6,075,906	System and Method for the Scaling of Image Streams that Use Motion Vectors	5/13/2000
	US	6,365,585	Video Camera Used with Personal Computer	12/28/1995
	US	5,804,856	Flow Control System Having a Counter in Transmitter for Determining and Incrementing Based Upon Transmitting and Received Message Size Respectively for Indicating Free Space in Receiver	2/18/1997
	US	5,311,329	Digital Filtering for Lenticular Printing	5/10/1994
	US	5,438,428	Digital Filtering for Lenticular Printing	8/1/1995
	US	6,070,002	System Software for Use in a Graphics Computer System Having a Shared System Memory	3/30/2000
	US	5,648,186	System and Method for a Computer-Based Dynamic Information Clipping Service	
Internet	US	5,737,560	Graphical Method and System for Accessing Information on a Communications Network	4/7/1998
	US	5,877,767	Graphical Method and System for Accessing Information on a Communications Network	3/2/1999
	US	5,742,768	System and Method for Providing and Displaying a Web Page Having an Embedded Menu	
	US	5,890,170	Method and Apparatus for Publishing Documents in a Hypertextual Network Environment	3/30/1999
	US	6,026,433	Method of Creating and Editing a Web Site in a Client-Server Environment Using Customizable Web Site Templates	2/15/2000
	US	6,072,491	Method and Computer Program Product for Accessing a Web Site	6/6/2000
	US	6,095,096	Web Site Delivery	8/1/2000
	US	6,098,092	Server to Dynamically Generate Graphics for the World Wide Web	8/1/2000
	US	6,189,029	Web Survey Tool Builder and Result Compiler	2/13/2001
	US	6,081,829	General Purpose Web Annotations Without Modifying Browser	6/27/2000
	US	6,199,098	Method and Apparatus for Providing an Expandable, Hierarchical Index in a Hypertextual, Client-Server Environment	3/6/2001
	US	6,012,058	Mechanism for Integrated Information Search and Retrieval from Diverse Sources Using Multiple Navigation Methods	1/4/2000
	US	4,772,681	Pixel Mapping Apparatus for Color Graphics Display	9/20/1988
	US	5,038,297	Method and Apparatus for Clearing a Region of Z-Buffer	8/6/1991
	US	5,187,125	Clock Switching Circuit for Asynchronous Clocks of Graphics Generation Apparatus	3/23/1993
Graphics Rendering				

## Schedule A - U.S. Patents

Country	Patent No.	Title	Issue Date
			9/13/1988
US	4,771,278	Dual Clock Shift Register	
			8/21/1990
US	4,851,232	Method for updating Pipelined Single Port Z-Buffer by Segments on a Scan Line	
US	4,788,827	Interleaved Pipeline Parallel Processing Architecture	12/8/1988
US	5,113,890	Method for Forming a Computer Model from an Intersection of a Cutting Surface with a Bounded Volume	5/12/1992
US			1/28/1993
US	5,182,548	Method and Apparatus for Painting on a Computer	
US	4,991,110	Graphics Processor with Staggered Memory Timing	2/5/1991
US	5,128,059	Graphics Processor with Staggered Memory Timing	7/7/1992
US	5,183,145	Method and Apparatus for Producing a Visually Improved Image in a Computer System	3/9/1993
US	5,847,700	Integrated Apparatus for Displaying a Plurality of Modes of Color Information on a Computer Output Display	12/8/1998
US			12/6/1994
US	5,371,818	Video Timing and Display ID Generator	
			4/15/1997
	5,621,432	Method and Apparatus for Generating Display Identification Information	
US	5,051,737	Efficient Graphics Process for Clipping Polygons	8/24/1991
US	5,265,941	Apparatus and Method for Controlling Storage of Display Information in a Computer System	11/30/1993
US	5,384,170	Apparatus and Method for Controlling Storage of Display Information in a Computer System	2/28/1995
US	5,768,852	Graphical Representation of Computer Network Topology and Activity	8/18/1998
US	5,585,824	Graphics Memory Apparatus and Method	12/17/1998
US	5,818,433	Graphics Memory Apparatus and Method	10/6/1998
US	5,307,450	Z-Subdivision for Improved Texture Mapping	4/28/1994
US	5,230,039	Texture Range Controls for Improved Texture Mapping	7/20/1993
US	5,343,558	Method for Scan Converting Shaded Triangular Polygons	8/30/1994
US	5,347,818	Method for Display Rendering by Determining the Coverage of Pixels in Polygons	9/13/1994
US	5,345,252	High Speed Cursor Generation Apparatus	9/6/1994
US	6,286,199	Method and Apparatus for Accomplishing Z-Buffering by Prediction	11/23/1993
US	5,548,709	An Apparatus and Method for Integrating Texture Memory and Interpolation Logic in a Computer System	8/20/1998
US	5,706,481	Apparatus and Method for Integrating Texture Memory and Interpolation Logic in a Computer System	1/8/1998
US	5,684,838	Antialiased Imaging with Improved Pixel Supersampling	11/4/1997
US	6,072,500	Antialiased Imaging with Improved Pixel Supersampling	8/6/2000
US	5,581,880	Method and Apparatus for Antialiasing Raster Scanned Images	12/3/1996
US	5,836,338	Method for Designing Curved Shapes for Use by a Computer	6/3/1997
US	5,515,484	Method and Apparatus for Rendering Volumetric Images	5/7/1998
US	5,389,738	Apparatus and Method for Generating Point Sample Masks in a Graphics Display System	11/29/1994
US	8,528,737	Processor-Based Method for Rasterizing Polygons at an Arbitrary Precision	6/18/1998
US	5,805,782	A Method and Apparatus for Projective Texture Mapping Rendered from Arbitrarily Positioned and Oriented Light Source	8/9/1998
US	5,682,584	Apparatus and Method for Handling Data Transfer Between a General Purpose Computer and a Cooperating Processor	10/28/1997
US			

## Schedule A - U.S. Patents

Country	Patent No.	Title	Issue Date
US	5,457,778	System for Accessing Graphic Data in a SIMD Processing Environment	10/10/1995
US	5,671,401	Apparatus for Efficiently Accessing Graphic Data for Rendering on a Display	9/23/1997
US	5,742,749	A Method and Apparatus for Shadow Generation Through Depth Mapping	4/21/1998
US	5,480,240	A System and Method of Generating Interactive Computer Graphic Images Incorporating Three Dimensional Textures	2/8/1996
US	5,438,554	System and Method for Sharpening Texture Imagery in Computer Generated Interactive Graphics	8/1/1995
US	5,455,827	Programmable Video Output Format Generator	10/3/1995
US	5,471,572	A System and Method for Adding Detail to Textures Imagery in Computer Generated Interactive Graphics	11/28/1995
US	5,704,024	A Method & Apparatus for Generating Reflection Vectors Which can be Unnormalized and For Using These Reflection Vectors to Index Locations on an Environment Map	12/30/1997
US	5,848,082	An Efficient Algorithm for Computer Texture Coordinates for Lines & Polygons	7/15/1997
US	5,628,738	Method and Apparatus for Antialiasing Raster Scanned, Polygonal Shaped Images	8/18/1996
US	5,618,597	Method for Sampling a Uniform Spatially-Distributed Sequence of Pixels in a Block	8/8/1997
US	5,710,878	A Computer Graphics System for Rendering Images Using Full Spectral Illumination Data	1/29/1998
US	5,835,133	Optical System for Single Camera	11/10/1998
US	5,808,461	Programmable Video Frame Detector	3/4/1997
US	5,703,810	Improved DRAM for Texture Mapping	12/30/1997
US	5,843,058	Texture Mapping Circuit for Performing Data Interpolations	8/24/1999
US	5,982,938	System and Method for Antialiasing of Texture Edges	11/8/1999
US	5,844,567	Computer Graphics System and Method for Texture Mapping Using Triangular Interpolation	12/1/1998
US	5,819,017	Apparatus and Method for Selectively Storing Depth Information of a 3-D Image	10/6/1998
US	5,877,771	A Method and Apparatus for Supersampling Based on the Local Rate of Change in Texture	3/2/1999
US	5,760,783	A Method and Apparatus for Providing Texture Using a Selected Portion of a Texture MIP-MAP	8/2/1998
US	5,818,613	System and Method for Color Space Conversion	10/6/1998
US	5,738,988	Apparatus and Method for Accelerated Tiled Data Retrieval	4/7/1998
US	5,637,479	Hierarchical Display List Processing in Graphics Data Retrieval System	8/12/1997
US	5,935,626	Computer Graphics Silhouette Load Management	8/10/1999
US	5,845,874	System and Method for Creating Visual Images of Aircraft Wake Vortices	12/8/1998
US	6,228,003	Method for Rendering Silhouette and True Edges of 3-D Line Drawings with Occlusion	5/1/2001
US	6,131,189	System and Method to Efficiently Represent Allocated and Indirect Memory Operations in Static Single Assignment Form During Compilation	10/10/2000
US	5,946,113	System and Method for Color Space Conversion Using an Extended Color Space	8/31/1999
US	6,147,772	System and Method for Color Space Conversion Using an Extended Color Space	11/14/2000
US	5,831,820	System and Computer-Based Method for Creating Real-Time Mirror Reflections	11/3/1998

## Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
	US	5,815,162	Computational Low-Cost Anti-Aliased Bresenham Line Algorithm	9/29/1998
	US	6,249,289	Multi-Purpose High Resolution Distortion Correction	6/19/2001
	US	5,949,424	Method, System and Computer Program Product for Bump Mapping in Tangent Space	9/7/1999
	US	5,880,738	Method, System and Computer Program Product for Shading	3/9/1999
	US	6,163,319	Method, System and Computer Program Product for Shading	12/19/2000
	US	6,104,417	Unified Memory Computer Architecture with Dynamic Graphics Memory Allocation	8/16/2000
	US	6,154,784	Upstream Situated Apparatus and Method within a Computer System for Controlling Data Flow to a Downstream Situated Input/Output Unit	11/28/2000
	US	6,078,331	A Method and System for Efficiently Drawing Subdivision Surfaces for 3D Graphics	6/20/2000
	US	6,078,332	Real-Time Lighting Algorithm Using 3D Texture Mapping	6/20/2000
	US	6,175,367	A Method and System for Real Time Illumination of Computer Generated Images	1/16/2001
	US	6,002,406	System and Method for Storing and Accessing Data Representative of an Object in Various Levels of Detail	12/14/1998
	US	6,154,215	Method and Apparatus for Maintaining Multiple Representations of a Same Scene in Computer Generated Graphics	11/28/2000
	US	6,057,850	Blended Texture Illumination Mapping	5/2/2000
	US	5,907,862	Latching Assembly for a Computer	6/1/1999
	US	6,215,485	A Platform Independent Application Program Interface for Interactive 3D Scene Management	4/10/2001
	US	6,108,007	Method, System and Computer Program Product for Increasing Interpolation Precision Using Multi-Channel Texture Mapping	8/22/2000
	US	6,104,415	Method for Accelerating Modified Textured Cache Access	8/15/2000
	US	6,232,981	Method for Improving Texture Locality for Pixel Quads by Diagonal Level of Detail Calculation	5/15/2001
	US	6,232,978	Method, System and Computer Program Product for Fast Computation Using Parallel Multi-Channel Resampling and Blending	5/15/2001
	US	6,248,416	Method and Apparatus for Culling Polygons	6/12/2001
	US	6,238,413	Method and System for a RISC Graphics Pipeline Optimized for High Clock Speeds by Using Recirculation	5/22/2001
	US	6,078,548	A Packetized Command Interface to a Graphics Processor	6/13/2000
	US	6,230,177	Method and Apparatus for Performing Fast Fourier Transforms	5/8/2001
	US	6,262,810	Method and Apparatus for Efficiently Switching State in a Graphics Pipeline	6/26/2001
	US	6,128,838	Method and Apparatus for Calculating X to the Exponent of Y	10/3/2000
	US	6,133,901	Method and System for Width Independent Anti-Aliasing	10/17/2000
	US	6,061,425	Constant Multisample Image Coverage Mask	7/18/2000
	US	6,205,531	Method and Apparatus for Virtual Address Translation	3/20/2001
	US	6,268,668	Apparatus and Method for Extending Computational Precision of a Computer System Having a Modular Arithmetic Processing Unit	7/3/2001
	US	6,229,547	System and Method for Rendering Multi-Planar Refractions Using Bi-Linear Interpolation	5/8/2001

## Schedule A - U.S. Patents

	Country	Patent No.	Title	Issue Date
		6,288,881	Volumetric Three-Dimensional Fog Rendering Technique	7/31/2001
	US	6,163,320	Method and Apparatus for Radiometrically Accurate Texture-Based Lightpoint Rendering Technique	12/18/2000
	US			

## Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
Tablet Computing	US	196100	"Pen-Based Interface for a Notepad Computer"	11/20/1998
	US	196114	"Pen-Based Computer System"	11/20/1998
Graphics Rendering	US	09/005129	System and Method for the Direct Rendering of Curve Bounded Objects	1/8/1998
	US	191458	Floating Point Gamma Correction Method And System	11/12/1998
	US	09/217398	High Precision Texture Wrapping Method And Device	12/21/1998
	US	265487	Method And Device For Associating A Pixel With One Of A Plurality Of Regions In A Logarithm Or Cosine Space	3/9/1999
	US	072050	Method and System For Providing Texture Using A Selected Portion of a Texture Map	5/8/1998
	US	033663	Improved Chrome-Key Suppression Method And Apparatus	3/3/1998
	US	09/244275	Memory Chip For Use in a Unified Memory Architecture	2/3/1999
	US	244281	Memory Controller for Controlling Memory in a Computer System Having a Unified Memory Architecture	2/3/1999
	US	244254	VO Chip in a Computer System Having a Unified Memory Architecture	2/3/1999
	US	220078	System and Method for Morphing Based on Multiple Weighted Parameters	12/23/1998
	US	846626	A Method and System for Efficiently Evaluating and Drawing Nurbs Surfaces for 3D Graphics	4/25/1997
	US	898123	System and Method for Displaying Different Portions of an Object in Different LOD Levels	7/23/1997
	US	070808	System and Method for Displaying Different Portions of an Object in Different Levels of Detail	5/1/1998
	US	074027	Occlusion Culling For Complex Transparent Scenes in Computer Generated Graphics	5/8/1998
	US	137005	Method and System for Performing Rasterization in Producing Three-Dimensional Graphics Using YUV Color Space and Combining Same with Digital Video in YUV Color Space	8/20/1998
	US	035378	Subsampled Texture Edge Antialiasing	3/5/1998
	US	966537	Method and Apparatus for Providing Image and Graphics Processing Using a Graphics Rendering Engine	10/23/1997
	US	837793	Method, System and Computer Program Product for Providing Illumination in Computer Graphics Shading and Animation	9/25/1997
	US	09/048089	Method for Efficient Handling of Texture Cache Misses by Reorganization	3/26/1998
	US	09/081073	Method and Apparatus for Line Antialiasing by Gamma-Corrected Area Calculation	5/18/1998
	US	248135	System and Method for Rendering an Image	2/8/1999
	US	09/448907	A Packetized Command Interface to a Graphics Processor	11/23/1999
	US	09/548158	A Packetized Command Interface to a Graphics Processor	4/13/2000
	US	09/145818	Method and Apparatus For Rasterizing in a Hierarchical Tree Order	9/2/1998
	US	978755	Range Correct Layered Fog Model Using 3D Texture	11/28/1997
	US	111284	Backface Primitives Culling	7/6/1998
	US	201814	Multi-Threaded Texture Modulation for Axis Aligned Volume Rendering	12/1/1998
	US	247422	Multiresolution Dither Method With Exact Reconstruction	2/10/1999



## Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	08/227227	Method and Apparatus For Synchronizing Graphics Pipelines	1/8/1998
	US	218121	Scalable Network Based Computer Graphics System	12/21/1998
	US	348071	Computer System Having A Distributed Texture Memory Architecture	7/1/1999
	US	08/707418	Method and Apparatus for Radiometrically Accurate Texture-Based Lightpoint Rendering Technique	11/8/2000
	US	348882	Antialiasing Method Using Barycentric Coordinates Applied to Lines	7/1/1999
	US	08/220596	System and Method For Translating Between Two Filters, Allowing For The Use of Higher Order Interpolation	12/28/1998
	US	08/416055	Method, System, and Computer Program Product For Compositing True Colors and Intensity-Mapped Colors Into A Frame Buffer	10/12/1999
	US	218042	System and Method For Locking Disparate Video Formats	12/22/1998
	US	08/247885	Method For Interfacing to Ultra-High Resolution Output Devices	2/10/1999
	US	232860	Method For Tiling Multiple Displays to Generate a Large Area Display of Moving Data	1/15/1999
	US	296546	Combined Floating-Point Logic Core and Frame Buffer Apparatus and Method for Sharing Antialiasing Memory Across Multiple Displays	4/19/1999
	US	08/294450	Transformation Pipeline for the Computing Distortion Correction Geometry for any Design Eyepoint, Display Surface Geometry and Projector Position	3/28/1999
	US	277587	Antialiasing Method for Computer Graphics	3/23/1999
	US	365270	Processor for Geometry Transformations and Lighting Calculations	12/23/1998
	US	08/220196	System and Method for Maintaining Time Dependence in Conversions That Include Parallel Operations	3/8/1999
	US	263185	A Method and System for Efficiently Implementing Two Sided Vertex Lighting in Hardware	12/20/1999
	US	08/467581	Method and Apparatus for Texture Memory Management	3/24/1999
	US	276726	Method and Apparatus for Early Culling of Occluded Objects	2/9/1999
	US	08/247816	Method and System for Generating Light Values for a Set of Vertices	7/1/1999
	US	348541	Method and System for Dynamic Clock Frequency Adjustment for a Graphics Subsystem in a Computer	3/19/1999
	US	273247	Fine Grain Multi-Pass for Multiple Texture Rendering	5/7/1999
	US	308967	Method and System for Dynamic Texture Replication on a Distributed Memory Graphics Architecture	8/24/1998
	US	344805	Cache Memory For High Latency and Out-of-Order Return of Texture Data	7/1/1999
	US	345365	Method for Virtual Clipping A Three-Dimensional Graphics Image	8/8/1999
	US	328000	A Method and System For Efficient Simplification of Tetrahedral Meshes Used in 3D Volumetric Representations	2/11/2000
	US	08/602497		

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Country	Application No.	Title	Filing Date
US	09/377778	Method, System and Computer Program Product for Multi-Pass Bump-Mapping into an Environment Map	8/20/1999
US	328164	Method and Apparatus for a Modified Linear Filter Using Texture Data as Phase Angle	8/8/1999
US	303894	View-Dependent Layer Ordering Method and System	5/3/1999
US	300918	Method and System for Iterative Morphing	4/28/1999
US	283269	Apparatus and Method for Increasing the Bandwidth to a Graphics Subsystem	4/19/1999
US	285483	Device, Method and System for Generating Per-Pixel Light Values Using Texture Parameters	3/9/1999
US	386379	Method, System, and Computer Program Product for Efficient Buffer Level Management of Memory-Buffered Graphics Data	8/31/1999
US	386378	Method, System and Computer Program Product for Overlapping Graphics Data Collection and Transmission Using a Single Processor	8/31/1999
US	220082	Method, System and Computer Program Product for Modified Blending Between Clip-Map Tiles	12/23/1998
US	09/383358	Reflection Space Image Based Rendering	8/8/1999
US	09/279865	Texture Generating Apparatus For Dynamic Instance Checking	11/30/2000
US	09/678879	Method and System for Evaluating Derivatives in Screen Space Using Perspective Corrected Barycentric Coordinates	9/28/2000
US	363636	Method and System for Transforming Color Coordinates by Direct Calculation	7/30/1999
US	09/676740	Cheap, Well-Behaved Affine Transformation of Bounding Spheres	5/23/2000
US	348885	Dual Mode Device and Method for Generating Vector Cross Products or DOT Products	7/1/1999
US	408851	System and Method for Load Balancing in a Multi-Channel Graphics System	9/30/1999
US	09/404808	Method, System, and Computer Program Product for Using Alpha Values to Control Pixel Blending	9/24/1999
US	09/461345	Method, System, and Computer Program Product for Generating Statically Varying Effects in a Digital Image	12/15/1999
US	09/583562	Method and Apparatus for Rendering a Quadrangle Primitive	6/4/2000
US	60/295864	Reducing Fill and Improving Quality of Intersected Displays Using Multi-Sampling	8/8/2001
US	08/832138	Scene Representation Method and System	4/10/2001
US	09/588649	Method and System for Implementing Graphics Control Constructs	6/12/2000
US	09/589650	Texture Indexing System and Method	5/12/2000
US	09/589621	Method and System for Accelerating Noise	5/12/2000
US	09/589520	Data Retrieval Method and System	5/12/2000
US	09/589654	Extended Range Pixel Display Method and System	
US	09/608988	Method, System and Computer Program Product for Implementing Derivative Operators with Graphics Hardware	8/29/2000
US	09/638907	Method and System for Executing SIMD Instruction Using Graphics Technology	8/15/2000
US	09/572452	Method, System, and Computer Program Product for Simulating Camera Depth-of-Field Effects in a Digital Image	5/17/2000
US	09/480668	An Efficient Graphics Pipeline With a Pixel Cache and Data Pre-Fetching	12/22/1999

## Schedule A - Pending Patent Applications

	Country	Application No.	Title	Filing Date
	US	09/470948	A System and Method for Linearly Mapping a Tiled Image Buffer	12/22/1998
	US	09/473208	A Graphics Geometry Cache	12/27/1999
	US	09/473210	View Volume Clip-Check in Object Space	12/27/1999
	US	09/584810	Method, System, and A Computer Program Product For Filtering A Texture Applied To A Surface Of A Computer Generated Object	10/10/2000
	US	09/599971	Method and System for Performing Multi-Texturing Operations Using Recursive Interleaving	6/21/2000
	US	09/588978	System and Method for Efficiently Controlling a Graphics Rendering Pipeline	10/17/2000
	US	60/255883	Method, System and Computer Program Product for Determining Regions That Are Occluded From An Observation Point	12/18/2000
	US	09/584812	Method, System, and Computer Program Product for Anisotropic Filtering and Applications Thereof	10/10/2000
	US	60/253948	System, Method, and Computer Program Product For General Environment Mapping	11/30/2000
	US	60/258325	Hardware-Accelerated Volume Lighting Algorithm	12/28/2000
	US	60/252094	Rendering Volumetric Fog and Other Gaseous Phenomena	11/21/2000
	US	60/238128	Texture Tiling With Adjacency Information	10/6/2000
	US	60/298416	A Volumetric Based Method and System for Visualizing Datasets	6/18/2001
	US	60/252092	Rendering Volumetric Fog and Other Gaseous Phenomena Using an Alpha Channel	11/21/2000
	US	60/252093	Method, System, and Computer Program Product for Rendering Multicolored Layered Fog with Self-Shadowing and Scene Shadowing	11/21/2000
	US	357829	General Purpose Web Annotations Without Modifying Browser	7/18/1999
Internet	US	09/557148	Graphical Method and System for Accessing Information on a Communications Network	4/25/2000
	US	09/615037	System and Method for Media Stream Indexing and Synchronization	10/7/1999
Digital Media	US	09/632558	Workstation for Processing and Producing a Video Signal	8/4/2000
	US	09/632862	System and Method for Pre-Processing a Video Signal	8/4/2000
	US	09/632462	System and Method for Producing a Video Signal	8/4/2000
	US	09/633461	System and Method for Packing and Unpacking Video Data	8/4/2000
	US	08/800421	Synchronizing Motion and Time-Based Data for Transfer Between a Server and a Client	7/25/1997
	US	09/441728	Seamless Playback of Multiple Clips of Media Data Across a Data Network	11/18/1999
	US	09/441722	Frame-Accurate Transport of Media Data Across a Data Network	11/18/1999
	US	08/030072	Backchannel Network Movie Player	2/6/1998
	US	035687	Preemptive Time Multiplexed Shared Memory Access	3/5/1998
	US	09/427197	Video Assistance System with Computer Generated Imagery Overlay	10/25/1999
	US	09/099742	System and Method Using a Packetized Encoded Bitstream for Parallel Compression and Decompression	6/18/1998
	US	09/518309	A Method and System for Efficiently Streaming 3D Animation Across a Wide Area Network	3/6/2000
	US	09/465388	Constant Bitrate Algorithm for Block Based Image Compression	12/17/1999
	US	09/639365	System and Method for Communicating Video Data in a Digital Media Device	3/3/2000

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	Country	Application No.	Title	Filing Date
	US	09/577171	Cost-Optimal Convolution Algorithm With Low or Null Latency	5/23/2000
	US	09/544360	A Technique For Controlling Media Data Streams using Embedded Controls	4/5/2000
	US	08/521234	Apparatus and Method for Recognizing Color Space of a Digital Video Input	3/8/2000
	US	09/528796	System and Method for Compressing Data	7/25/2000
	US	09/628658	System and Method for Storing Compressed Data onto a Storage Medium	7/28/2000
	US	60/281106	Method and Apparatus for Producing Digital Video from a General Digital Graphics Interface	

## Schedule A - Foreign Patents and Applications

Country	App/Patent No.	Title	Foreign Filing Date	Issue Date	Status
EPO	95946509.8	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Abandoned
Japan	2000-513245	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Pending
PCT	US98/20098	Method, System And Computer Program Product For Providing Illumination In Computer Graphics Shading And Animation	9/25/1998		Pending
France	99042475.7	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	5/17/2000		Pending
Japan	2000-868069	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	6/2/2000		Pending
PCT	US99/18363	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	6/23/1999		Pending
UK	99042475.7	Method And Apparatus For Rasterizing In A Hierarchical Tile Order	6/17/2000		Pending
EPO	990207.9	Method And Apparatus For Synchronizing Graphics Pipelines	7/1/2001		Pending
Japan		Method And Apparatus For Synchronizing Graphics Pipelines	7/6/2001		Pending
PCT	US00/00649	Method And Apparatus For Synchronizing Graphics Pipelines	1/7/2000		Pending
PCT	US00/10634	Combined Floating-Point Logic Core And Frame Buffer	4/19/2000		Pending
PCT	US00/167904	Method And Apparatus For Texture Memory Management	3/24/2000		Pending
PCT	US00/10578	Apparatus And Method For Increasing The Bandwidth To A Graphics Subsystem	4/19/2000		Pending
PCT	US00/08184	Device, Method And System For Generating Per-Pixel Light Values Using Texture Parameters	3/8/2000		Abandoned
PCT	US00/40788	Method, System And Computer Program Product For Overlapping Graphics Data Collection And Transmission Using A Single Processor	6/5/2000		Pending
PCT	US99/29984	Method, System And Computer Program Product For Modified Blending Between Clip-Area Tiles	12/17/1999		Pending
PCT	US00/21381	Workstation For Processing And Producing A Video Signal	6/4/2000		Abandoned
PCT	US00/21382	System And Method For Pre-Processing A Video Signal	6/4/2000		Abandoned
PCT	US00/21383	System And Method For Producing A Video Signal	6/4/2000		Abandoned
PCT	US00/21380	System And Method For Packing And Unpacking Video Data	6/4/2000		Pending
EPO	99009763.7	Preemptive Time Multiplexed Shared Memory Access	3/2/1999		Pending
Japan	2000-034847	Preemptive Time Multiplexed Shared Memory Access	3/2/1999		Pending
PCT	US99/04616	Preemptive Time Multiplexed Shared Memory Access	7/29/2001		Pending
PCT		System And Method For Compressing Data	7/27/2001		Pending
PCT		System And Method For Storing Compressed Data On A Storage Medium		10/15/1991	Granted
Canada	1290870	Pixel Mapping Apparatus For Color Graphics		11/27/1997	Granted
Germany	083738195	Pixel Mapping Apparatus For Color Graphics		6/9/1995	Granted
Hong Kong	527	Pixel Mapping Apparatus For Color Graphics		4/16/1999	Granted
Japan	2013098	Pixel Mapping Apparatus For Color Graphics		12/28/1991	Granted
Singapore	2193319	Pixel Mapping Apparatus For Color Graphics		6/28/1991	Granted
UK	2196318	Pixel Mapping Apparatus For Color Graphics		5/27/1994	Granted
India	172327	Dual Clock Shift Register		5/29/1995	Granted
EPO	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		6/29/1996	Granted
France	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/29/1996	Granted
Germany	DE98028571	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line			Granted

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Country	App/Patent No.	Title	Foreign Filing Date	Issue Date	Status
Japan	2884280	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		2/12/1996	Granted
Korea	131620	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		12/4/1997	Granted
Switzerland	433373	Method For Updating Pipelined Single Port Z-Buffer By Segments On A Scan Line		5/29/1998	Granted
Canada	1276312	Interleaved Pipeline Parallel Processing Architecture		11/13/1990	Granted
EPO	05912783.8	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		Pending
PCT	US95/02853	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		Abandoned
JP	9510309	An Apparatus And Method For Integrating Texture Memory And Interpolation Logic In A Computer System	3/7/1995		
EPO	08925193	Apparatus And Method For Selectively Storing Depth Information Of A 3-D Image	8/15/1995		Abandoned
PCT	US96/13245	Apparatus And Method For Selectively Storing Depth Information Of A 3-D Image	8/15/1995		Abandoned
EPO	08928541.7	A Method And Apparatus For Providing Texture Using A Selected Portion Of A Texture Mip-Map	11/8/1996		Abandoned
PCT	US96/17873	A Method And Apparatus For Providing Texture Using A Selected Portion Of A Texture Mip-Map	11/8/1996		Abandoned
EPO	97017281.8	System And Method For Color Space Conversion Using An Extended Color Space	3/28/1997		Abandoned
PCT	US97/04081	System And Method For Color Space Conversion Using An Extended Color Space	3/28/1997		Abandoned
Canada	2214866	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	8/9/1997		Pending
EPO	97308997.8	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	8/9/1997		Pending
Japan	251134.97	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	8/16/1997		Pending
Mexico	978486	Unified Memory Computer Architecture With Dynamic Graphics Memory Allocation	8/28/1997		Pending
PCT	US98/15075	Method And Apparatus For Virtual Address Translation	7/1/1999		Abandoned
PCT	US98/18802	System and Method for Combining Multiple Video Streams	6/26/1999		Abandoned
PCT	US98/0844	Digital Filtering for Lenticular Printing	8/18/1992		Abandoned
AU	9225485	Digital Filtering for Lenticular Printing	8/18/1992		Abandoned
PCT	US99/3924	Method and Apparatus for Clearing a Region of Z-Buffer	8/12/1999	2/7/1999	Abandoned
EP	531251	Method and Apparatus for Clearing a Region of Z-Buffer	8/12/1999	3/21/1999	Abandoned
DE	68926631	Method and Apparatus for Clearing a Region of Z-Buffer	published 3/22/1999		Abandoned
PCT	WO99/02981	Graphics Processor with Staggered Memory Timing	published 8/22/1991		Abandoned
PCT	WO91/12688	Method and Apparatus for Producing a Visually Improved Image in a Computer System	published 8/3/1991		Abandoned
AU	9173134	Method and Apparatus for Producing a Visually Improved Image in a Computer System			
PCT	US00/18748	Computer System Having A Distributed Texture Memory Architecture	8/18/2000		Pending

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